

## FEE TRANSMITTAL

Electronic Version v08

Stylesheet Version v08.0

**Title of  
Invention**

Method and Apparatus for Creating Circuit Redundancy in Programmable Logic Devices

Application Number :

Date :

First Named Applicant: Praveen K Samudrala

Attorney Docket Number: 1372.136.PRC

**TOTAL FEE AUTHORIZED \$ 448**

Patent fees are subject to annual revisions on or about October 1st of each year.

Filing as small entity

**BASIC FILING FEE**

Fee Description	Fee Code	Amount \$	Fee Paid \$
Utility Filing Fee	2001	385	385
Subtotal For Basic Filing Fees: \$ 385			

**EXTRA CLAIM FEES**

Fee Description	Extra Claim	Fee Code	Amount \$	Fee Paid \$
Total Claims : 27	7	2202	9	63
Independent Claims : 3	0	2201	43	0
Subtotal For Extra Claims Fees: \$ 63				

**AUTHORIZED BILLING INFORMATION**

**The commissioner is hereby authorized to charge indicated fees and credit any overpayments to:**

Credit account number: 3008  
Expiration Date (YYYYMMDD): 2004-05-31  
Authorized name: Anton J Hopen  
Billing address: 33760